



# Superscalar Microprocessors Design

*Mike Johnson*

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## **Superscalar Microprocessors Design** Mike Johnson

The term superscalar describes a computer architecture that achieves performance by concurrent execution of scalar instructions. Superscalar architectures represent the next step in the evolution of microprocessors. This book is intended as a technical tutorial and introduction for engineers & computer scientists. The book concentrates on reduced instruction set (RISC) processors.

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